

TL081CP JFET 入力 オペアンプ

Number of channels (#) 1
Total supply voltage (Max) (+5V=5, +/-5V=10) 30
Total supply voltage (Min) (+5V=5, +/-5V=10) 7
Rail-to-rail In to V+
GBW (Typ) (MHz) 3
Slew rate (Typ) (V/us) 13
Vos (offset voltage @ 25 C) (Max) (mV) 6
Iq per channel (Typ) (mA) 1.4
Vn at 1 kHz (Typ) (nV/rtHz) 18
Rating Catalog
Operating temperature range (C) -40 to 85, 0 to 70
Offset drift (Typ) (uV/C) 18
Features Standard Amps
Input bias current (Max) (pA) 200
CMRR (Typ) (dB) 86
Output current (Typ) (mA) 10
Architecture FET

特長

Low Power Consumption: 1.4 mA/ch Typical
Wide Common-Mode and Differential Voltage Ranges
Low Input Bias Current: 30 pA Typical
Low Input Offset Current: 5 pA Typical
Output Short-Circuit Protection
Low Total Harmonic Distortion: 0.003% Typical
High Input Impedance: JFET Input Stage
Latch-Up-Free Operation
High Slew Rate: 13 V/ μ s Typical
Common-Mode Input Voltage Range
Includes VCC+

■Ti社の意向により、データシートの掲載を終了いたしました。

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