

## TL074CN RoHS クワッド 低雑音 JFET入力 汎用オペアンプ

Number of channels (#) 4  
Total supply voltage (Max) (+5V=5, +/-5V=10) 30  
Total supply voltage (Min) (+5V=5, +/-5V=10) 7  
Rail-to-rail In to V+  
GBW (Typ) (MHz) 3  
Slew rate (Typ) (V/us) 13  
Vos (offset voltage @ 25 C) (Max) (mV) 6  
Iq per channel (Typ) (mA) 1.4  
Vn at 1 kHz (Typ) (nV/rtHz) 18  
Rating Catalog  
Operating temperature range (C) -40 to 85, 0 to 70  
Offset drift (Typ) (uV/C) 18  
Features Standard Amps  
Input bias current (Max) (pA) 200  
CMRR (Typ) (dB) 100  
Output current (Typ) (mA) 10  
Architecture FET

### 特長

Low Power Consumption  
Wide Common-Mode and Differential Voltage Ranges  
Low Input Bias and Offset Currents  
Output Short-Circuit Protection  
Low Total Harmonic Distortion: 0.003% (Typical)  
Low Noise  
Vn = 18 nV/ $\sqrt{\text{Hz}}$  (Typical) at f = 1 kHz  
High-Input Impedance: JFET Input Stage  
Internal Frequency Compensation  
Latch-Up-Free Operation  
High Slew Rate: 13 V/ $\mu\text{s}$  (Typical)  
Common-Mode Input Voltage Range  
Includes VCC+

■Ti社の意向により、データシートの掲載を終了いたしました。

■TI社のWebサイトを参照する様にお願いいたします。