

THS4521IDGKR RoHS

超低消費電力、レール・ツー・レール出力、完全差動アンプ

Number of channels (#) 1
Total supply voltage (Min) (+5V=5, +/-5V=10) 2.5
Total supply voltage (Max) (+5V=5, +/-5V=10) 5.5
BW @ Acl (MHz) 145
Acl, min spec gain (V/V) 1
Slew rate (Typ) (V/us) 490
Vn at flatband (Typ) (nV/rtHz) 4.6
Iq per channel (Typ) (mA) 1.14
Rail-to-rail In to V-, Out
Vos (offset voltage @ 25 C) (Max) (mV) 2
Operating temperature range (C) -40 to 85
Output current (Typ) (mA) 55
2nd harmonic (dBc) 133
3rd harmonic (dBc) 141
GBW (Typ) (MHz) 95
Input bias current (Max) (pA) 900000
@ MHz 0.001
CMRR (Typ) (dB) 102

特長

Fully Differential Architecture

Bandwidth: 145 MHz (AV = 1 V/V)

Slew Rate: 490 V/ μ s

HD2: -133 dBc at 10 kHz (1 VRMS,
RL = 1 k Ω)

HD3: -141 dBc at 10 kHz (1 VRMS,
RL = 1 k Ω)

Input Voltage Noise: 4.6 nV/ $\sqrt{\text{Hz}}$
(f = 100 kHz)

THD+N: -112dBc (0.00025%) at
1 kHz (22-kHz BW, G = 1, 5 VPP)

Open-Loop Gain: 119 dB (DC)

NRI-Negative Rail Input

RRO-Rail-to-Rail Output

Output Common-Mode Control
(with Low Offset)

Power Supply:

Voltage: +2.5 V (± 1.25 V) to
+5.5 V (± 2.75 V)

Current: 1.14 mA/ch

Power-Down Capability: 20 μ A (typical)

■Ti社の意向により、データシートの掲載を終了いたしました。

■TI社のWebサイトを参照する様にお願いいたします。