

## TL061CP RoHS 低消費電力 JFET入力 汎用オペアンプ

Number of channels (#) 1  
Total supply voltage (Max) (+5V=5, +/-5V=10) 30  
Total supply voltage (Min) (+5V=5, +/-5V=10) 7  
Rail-to-rail In to V+  
GBW (Typ) (MHz) 1  
Slew rate (Typ) (V/us) 3.5  
Vos (offset voltage @ 25 C) (Max) (mV) 6  
Iq per channel (Typ) (mA) 0.2  
Vn at 1 kHz (Typ) (nV/rtHz) 42  
Rating Catalog  
Operating temperature range (C) -40 to 85, 0 to 70  
Offset drift (Typ) (uV/C) 10  
Features Standard Amps  
Input bias current (Max) (pA) 200  
CMRR (Typ) (dB) 86  
Output current (Typ) (mA) 10  
Architecture FET

### 特長

Very Low Power Consumption  
Typical Supply Current: 200  $\mu$ A (Per Amplifier)  
Wide Common-Mode and Differential Voltage Ranges  
Low Input Bias and Offset Currents  
Common-Mode Input Voltage Range  
Includes VCC+  
Output Short-Circuit Protection  
High Input Impedance: JFET-Input Stage  
Internal Frequency Compensation  
Latch-Up-Free Operation  
High Slew Rate: 3.5 V/ $\mu$ s Typical  
On Products Compliant to MIL-PRF-38535,  
All Parameters Are Tested Unless Otherwise  
Noted. On All Other Products, Production  
Processing Does Not Necessarily Include  
Testing of All Parameters.

■Ti社の意向により、データシートの掲載を終了いたしました。

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