

LM324DRG4 RoHS クワッド、オペアンプ

Number of channels (#) 4
Total supply voltage (Max) (+5V=5, +/-5V=10) 32
Total supply voltage (Min) (+5V=5, +/-5V=10) 3
Rail-to-rail In to V-
GBW (Typ) (MHz) 1.2
Slew rate (Typ) (V/us) 0.5
Vos (offset voltage @ 25 C) (Max) (mV) 7
Iq per channel (Typ) (mA) 0.175
Vn at 1 kHz (Typ) (nV/rtHz) 35
Rating Catalog
Operating temperature range (C) 0 to 70
Features Standard Amps
Input bias current (Max) (pA) 250000
CMRR (Typ) (dB) 80
Output current (Typ) (mA) 40
Architecture Bipolar

特長

Wide Supply Ranges

Single Supply: 3 V to 32 V

(26 V for LM2902)

Dual Supplies: ± 1.5 V to ± 16 V

(± 13 V for LM2902)

Low Supply-Current Drain Independent of

Supply Voltage: 0.8 mA Typical

Common-Mode Input Voltage Range Includes

Ground, Allowing Direct Sensing Near Ground

Low Input Bias and Offset Parameters

Input Offset Voltage: 3 mV Typical

A Versions: 2 mV Typical

Input Offset Current: 2 nA Typical

Input Bias Current: 20 nA Typical

A Versions: 15 nA Typical

Differential Input Voltage Range Equal to

Maximum-Rated Supply Voltage:

32 V (26 V for LM2902)

Open-Loop Differential Voltage Amplification:

100 V/mV Typical

Internal Frequency Compensation

■Ti社の意向により、データシートの掲載を終了いたしました。

■TI社のWebサイトを参照する様にお願いいたします。