

TLC272CP RoHS デュアル単一電源オペアンプ

Number of channels (#) 2
Total supply voltage (Max) (+5V=5, +/-5V=10) 16
Total supply voltage (Min) (+5V=5, +/-5V=10) 3
Rail-to-rail In to V-
GBW (Typ) (MHz) 2
Slew rate (Typ) (V/us) 3.6
Vos (offset voltage @ 25 C) (Max) (mV) 10
Iq per channel (Typ) (mA) 0.7
Vn at 1 kHz (Typ) (nV/rtHz) 25
Rating Catalog
Operating temperature range (C) 0 to 70, -40 to 85
Offset drift (Typ) (uV/C) 1.8
Features —
Input bias current (Max) (pA) 60
CMRR (Typ) (dB) 80
Output current (Typ) (mA) 10
Architecture CMOS

特長

Trimmed Offset Voltage:

TLC277 . . . 500 uV Max at 25° C, VDD = 5 V

Input Offset Voltage Drift ...Typically 0.1 uV/Month, Including the First 30 Days

Wide Range of Supply Voltages Over Specified Temperature Range:

0° C to 70° C ...3 V to 16 V

-40° C to 85° C ...4 V to 16 V

-55° C to 125° C ...4 V to 16 V

Single-Supply Operation

Common-Mode Input Voltage Range Extends Below
the Negative Rail (C-Suffix, I-Suffix types)

Low Noise ...Typically 25 nV/Hz at f = 1 kHz

Output Voltage Range Includes Negative Rail

High Input impedance ...10¹² Typ

ESD-Protection Circuitry

Small-Outline Package Option Also Available in Tape and Reel

Designed-In Latch-Up Immunity

■Ti社の意向により、データシートの掲載を終了いたしました。

■TI社のWebサイトを参照する様にお願いいたします。