

# **HD74HC85**

# 4-bit Magnitude Comparator

REJ03D0555-0200 (Previous ADE-205-427) Rev.2.00 Oct 06, 2005

## **Description**

The HD74HC85 is designed for high speed comparison of two four bit words. This circuit has eight comparison input, 4 for each word; three cascade inputs (A < B, A > B, A = B); and three decision outputs (A < B, A > B, A = B). The result of a comparison is indicated by a high level on one of the decision outputs. Thus it may be determined whether one word is "greater than," "less than," or "equal to" the other word. By connecting the outputs of the least significant stage to the cascade inputs of the next stage, words of greater than four bits can be compared. In addition the least significant stage must have a high level applied to the A = B input, and a low level to the A < B, and A > B inputs.

### **Features**

• High Speed Operation:  $t_{pd}$  (Data Word Input to Output) = 20 ns typ ( $C_L = 50 \text{ pF}$ )

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2 \text{ to } 6 \text{ V}$ 

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC85P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74HC85FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74HC85RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

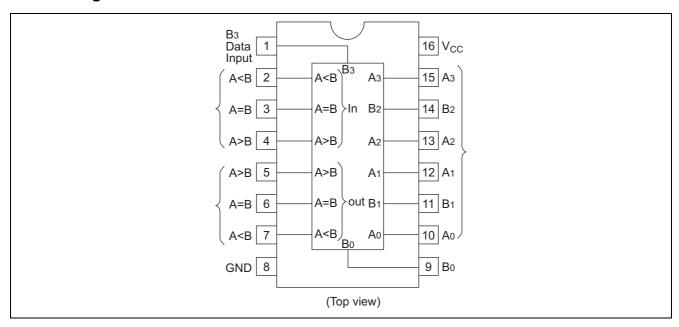
Note: Please consult the sales office for the above package availability.

# **Function Table**

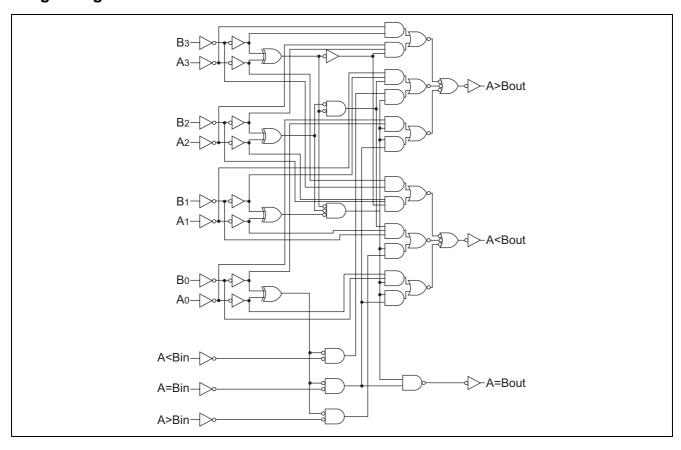
	Data Wo	rd Inputs		Ca	scading Inp	uts	Outputs		
A <sub>3</sub> , B <sub>3</sub>	A <sub>2</sub> , B <sub>2</sub>	A <sub>1</sub> , B <sub>1</sub>	A <sub>0</sub> , B <sub>0</sub>	A > Bin	A = Bin	A < Bin	A > Bout	A = Bout	A < Bout
$A_3 > B_3$	Х	Х	Х	Х	Х	Х	Н	L	L
A <sub>3</sub> < B <sub>3</sub>	Х	Х	Х	Х	Х	Х	L	L	Н
$A_3 = B_3$	$A_2 > B_2$	Х	Х	Х	Х	Х	Н	L	L
$A_3 = B_3$	A <sub>2</sub> < B <sub>2</sub>	Х	Х	Х	Х	Х	L	L	Н
$A_3 = B_3$	$A_2 = B_2$	A <sub>1</sub> > B <sub>1</sub>	Х	Х	Х	Х	Н	L	L
$A_3 = B_3$	$A_2 = B_2$	A <sub>1</sub> < B <sub>1</sub>	Х	Х	Х	Х	L	L	Н
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 > B_0$	Х	Х	Х	Н	L	L
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 < B_0$	Х	Х	Х	L	L	Н
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	L	L	L	Н	L	Н
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	L	L	Н	L	L	Н
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	Н	L	L	Н	L	L
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	Н	L	Н	L	L	L
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 = B_0$	Х	Н	Х	L	Н	L

H: High level
L: Low level
X: Irrelevant

# **Pin Arrangement**



# **Logic Diagram**



# **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	Vin, Vout	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	I <sub>0</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

ltem	Symbol	Ratings	Unit	Conditions	
Supply voltage	Vcc	2 to 6	V		
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V		
Operating temperature	Та	-40 to 85	°C		
		0 to 1000		V <sub>CC</sub> = 2.0 V	
Input rise / fall time <sup>*1</sup>	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns	V <sub>CC</sub> = 4.5 V	
		0 to 400		$V_{CC} = 6.0 \text{ V}$	

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

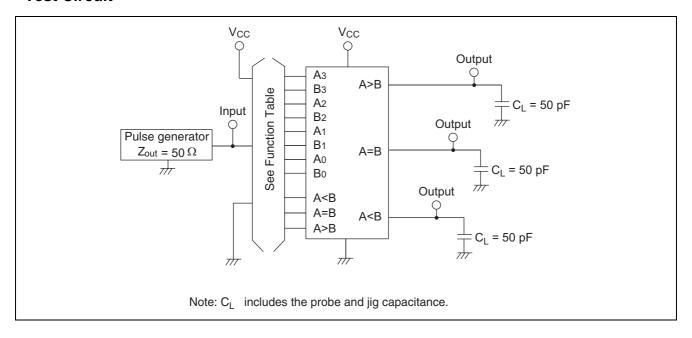
# **Electrical Characteristics**

			Т	a = 25°	С	Ta = -40 to+85°C				
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	V <sub>IL</sub>	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35			
		6.0	_	_	1.8	_	1.8			
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18	_	_	4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	_	0.0	0.1	_	0.1			
		4.5	_	_	0.26	_	0.33			$I_{OL} = 4 \text{ mA}$
		6.0	_	_	0.26	_	0.33			I <sub>OL</sub> = 5.2 mA
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent supply current	Icc	6.0	_	_	4.0	_	40	μΑ	Vin = V <sub>CC</sub> or GN	ID, lout = $0 \mu A$

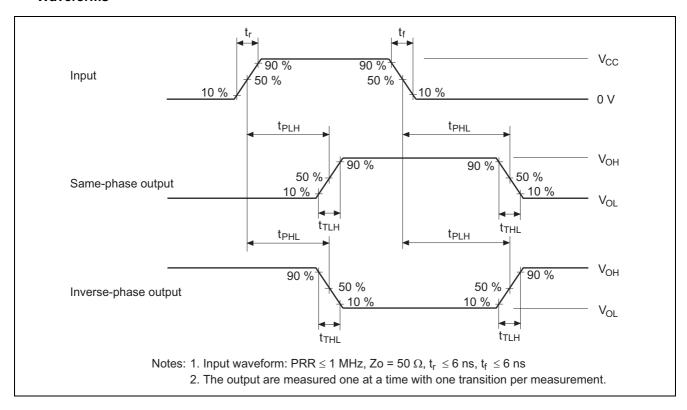
# Switching Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

			Т	a = 25°	С	Ta = -40	to +85°C		
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PLH</sub> , t <sub>PHL</sub>	2.0	_	_	210	_	265	ns	$C_0$ to $\Sigma_1$
time		4.5	_	20	42	_	53		
		6.0	_	_	36	_	45		
	t <sub>PLH</sub> , t <sub>PHL</sub>	2.0	_	_	175	_	220	ns	$A_1$ or $B_1$ to $\Sigma_1$
		4.5	_	20	35	_	44		
		6.0	_	_	30	_	37		
	t <sub>PLH</sub> , t <sub>PHL</sub>	2.0	_	_	125	_	155	ns	C <sub>0</sub> to C <sub>4</sub>
		4.5	_	12	25	_	31		
		6.0	_	_	21	_	26		
	t <sub>PLH</sub> , t <sub>PHL</sub>	2.0	_	_	155	_	195	ns	A <sub>1</sub> or B <sub>1</sub> to C <sub>4</sub>
		4.5	_	14	31	_	39		
		6.0	_	_	26	_	33		
Output fall time	t <sub>THL</sub>	2.0	_	_	75	_	95	ns	
		4.5	_	5	15	_	19		
		6.0	_		13		16		
Input capacitance	Cin	_	_	5	10	_	10	pF	

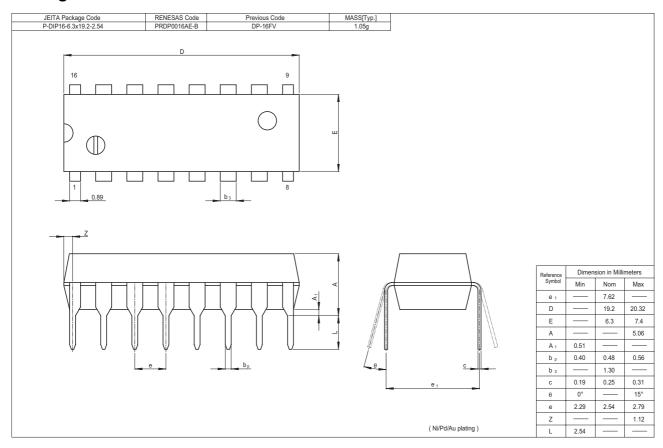
## **Test Circuit**

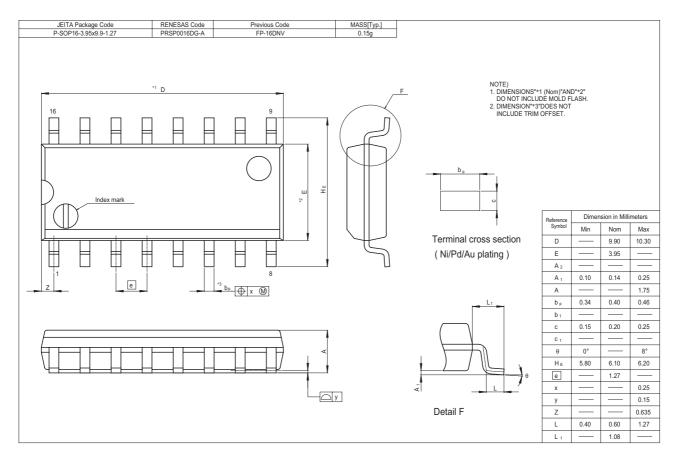


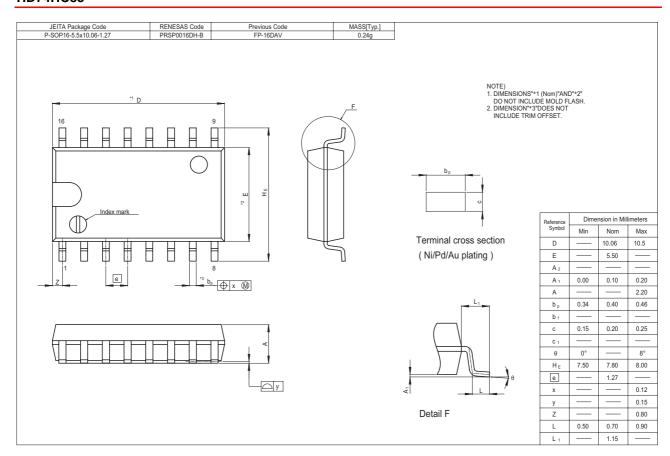
### **Waveforms**



## **Package Dimensions**







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