

# DN74LS38

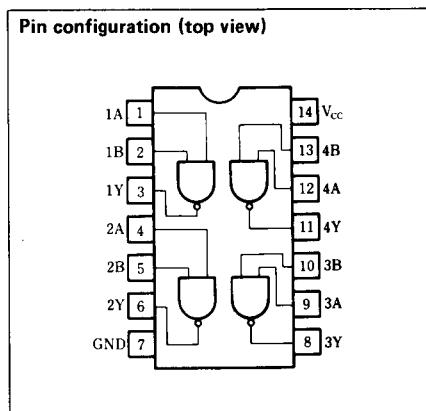
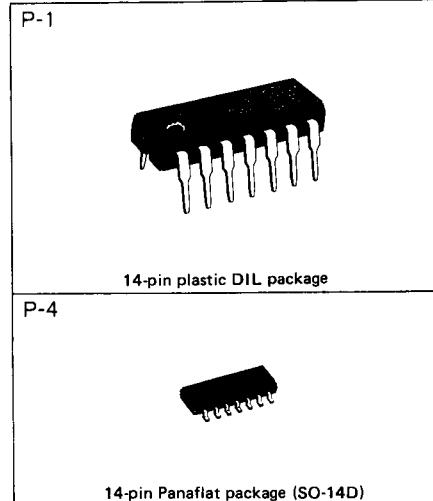
Quad 2-input Positive NAND Buffers (with Open Collector Outputs)

## ■ Description

DN74LS38 contains four 2-input positive isolation NAND buffer gate circuits with open collector outputs.

## ■ Features

- "Wired" AND capability
- High fan-out ( $I_{OL}$  = 24mA maximum)
- Low power consumption ( $P_d$  = 17.5mW typical)
- High speed ( $t_{pd}$  = 19ns typical)
- Wide operating temperature range ( $T_a$  = -20 to +75°C)



## ■ Recommended operating conditions

Parameter	Sym	Min	Typ	Max	Unit
Supply voltage	V <sub>cc</sub>	4.75	5.00	5.25	V
HIGH level output voltage	V <sub>OH</sub>			5.5	V
LOW level output voltage	I <sub>OL</sub>			24	mA
Operating temperature range	T <sub>opr</sub>	-20	25	75	°C

■ DC characteristics ( $T_a = -20 \sim +75^\circ\text{C}$ )

Parameter	Sym	Test conditions		Min	Typ*	Max	Unit	
Input voltage	$V_{IH}$			2.0			V	
	$V_{IL}$					0.8	V	
Output voltage	$V_{OL1}$	$V_{CC} = 4.75\text{V}$	$I_{OL} = 12\text{mA}$		0.25	0.4	V	
	$V_{OL2}$	$V_{IH} = 2\text{V}$	$I_{OL} = 24\text{mA}$		0.35	0.5	V	
Input current	$I_{IH}$	$V_{CC} = 5.25\text{V}$	$V_I = 2.7\text{V}$			20	$\mu\text{A}$	
	$I_{IL}$	$V_{CC} = 5.25\text{V}$	$V_I = 0.4\text{V}$			-0.4	mA	
	$I_I$	$V_{CC} = 5.25\text{V}$	$V_I = 7\text{V}$			0.1	mA	
Output current	$I_{OH}$	$V_{CC} = 4.75\text{V}, V_I = 0.8\text{V}$				250	$\mu\text{A}$	
Input clamp voltage	$V_{IK}$	$V_{CC} = 4.75\text{V}$				-1.5	V	
	$I_{ICL}$	$I_I = -18\text{mA}$				0.9	2.0	mA
Supply current	$I_{CCH}$	$V_{CC} = 5.25\text{V}$				6	12	mA
	$I_{CCL}$	$V_{CC} = 5.25\text{V}$						

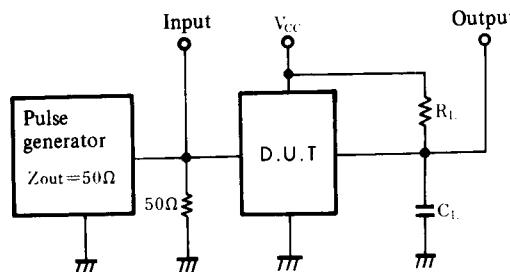
\* When constant at  $V_{CC} = 5\text{V}$ ,  $T_a = 25^\circ\text{C}$ .

■ Switching characteristics ( $V_{CC} = 5\text{V}$ ,  $T_a = 25^\circ\text{C}$ )

Parameter	Sym	Test conditions		Min	Typ	Max	Unit
Propagation delay time	$t_{PLH}$	$C_L = 45\text{pF}, R_L = 667\Omega$			20	32	ns
	$t_{PHL}$				18	28	ns

※ Switching parameter measurement information

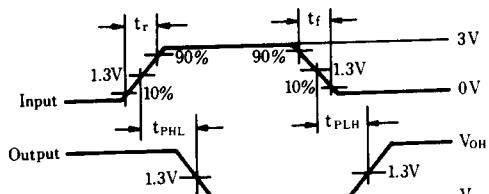
## 1. Measurement circuit



## Notes

1.  $C_L$  includes probe and tool floating capacitance.
2. Diodes are all MA161 or equivalent.

## 2. Waveforms



## Notes

1. Input waveform:  $t_r \leq 15\text{ns}$ ,  $t_f \leq 6\text{ns}$ , PRR = 1MHz, duty cycle = 50%.